



DEPARTMENT OF ELECTRICAL ENGINEERING

University College of Engineering (Autonomous)

Osmania University, Hyderabad-500 007

Telangana State, India, Phone: 040-27098628

No: UCE/EEE/DST/JRF/2018

Advertisement for the Post of 'JUNIOR RESEARCH FELLOW'

Applications are invited from candidates highly motivated towards research for a temporary post of **Junior Research Fellow (JRF)** to work in the Department of Science and Technology (DST) sponsored Indo-Sri Lanka Joint Project [DST/INT/SL/P-18/2016] entitled “**Design and Development of Hybrid Wind-Solar Power Generation System using Multilevel Inverter for Grid Connected Applications**”, sanctioned by the Department of Science and Technology, Ministry of Science and Technology, Government of India, New Delhi.

Details of Qualifications, Emoluments and Duration are:

Name of the position	Junior Research Fellow (JRF)/ 01 - post
Eligibility	Essential Qualifications: <ul style="list-style-type: none">• First Class in B.E. /B.Tech or equivalent in Electrical and Electronics Engineering/ Electrical Engineering and First Class in M.E./M.Tech with a specialization of Power Electronics/ Power Electronics and Drives. Desirable Qualification: <ul style="list-style-type: none">• Candidate should have adequate knowledge in Hybrid Power Generation system and Multilevel Inverters.• Candidate should have excellent programming skills in MATLAB/ PSIM.
Age limit	28 years
Duration	Three years or up to the termination of the project, subject to annual performance review
Consolidated Salary	<ol style="list-style-type: none">1. Rs. 25,000/- pm for first two years and Rs.28,000/- pm for third year.2. HRA as per institute rules

How to apply:

Interested and eligible candidates are required to apply in the prescribed application format attached with this advertisement. Duly filled in application form along with the supporting copies of certificates should be sent as hard copy by post to the address mentioned below with a superscription on the envelop “**Application for the post of Junior Research Fellow (DST)**” to reach by **21st April 2018**. Only short listed candidates will be called for interview and no TA/DA will be paid for attending the interview.

Address for correspondence:

Dr. P. Satish Kumar

Associate Professor & Principal Investigator

Department of Electrical Engineering

University College of Engineering

Osmania University, Hyderabad – 500 007

Telangana State, Email: satish_8020@yahoo.co.in



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Application Form

JRF Position in a DST funded Project [DST/INT/SL/P-18/2016]

1. Name in full (in BLOCK LETTERS) :

2. Date of Birth (DOB) :

3. Gender (M/F) :

4. Marital Status (Single/Married) :

5. Nationality :

6. Phone/ Mobile No :

7. Email address :

8. Address:

Correspondence:

Permanent:

Affix signed recent passport size photo

9. Qualifying Degree: (SSC onwards, attach the copy of proof)

S.No.	Degree	Discipline	University/ College	Regular/ Part-time	Year	% of Marks/C GPA
1						
2						
3						
4						
5						

10. Professional Experiences (Teaching/ Research/ Industrial)

Name of the Organization	Designation	Duration		Nature of Work
		From	To	

11. Computer Skills:

**12. Research Publications:
(Enclose Papers)**

13. Awards, Patents, Prizes:

14. Any other relevant information:

DECLARATION

I hereby declare that all the statement made in this application are true, complete and correct to the best of my knowledge and belief. In the event of the information being found false or incorrect or any ineligibility being detected before or after the selection, my candidature is liable to be cancelled.

Date:

Place:

Signature